



**INDIAN INSTITUTE OF TECHNOLOGY PATNA  
BIHTA PATNA-801106  
RESEARCH & DEVELOPMENT UNIT**

**ADVERTISEMENT NO:R&D/1008/DDN/403**

**DATED:14.08.2024**

Project No.: R&D/SP/EE/DDN/2023-24/1008

Applications are invited in the prescribed format only for the following assignment in a purely time bound research project undertaken in this institute.

1. (a) Name of the temporary assignment : **Junior Research Fellow (JRF)**  
(b) Number of Post : **01 (one)**  
(c) Duration of the Post :Initially for one year, extendable to another two years subject to satisfactory performance.
2. Name of the temporary research project :**Design and Development of Neuromorphic Quantum Computing Architecture for High Throughput and Energy Efficient Applications**
3. Name of the sponsoring Agency :**DST, SERB**
4. Consolidated Fellowship/Salary :**37,000/- per month + HRA**
5. Qualifications & Experience :B.Tech./B.E./M.Tech./ME/ Equivalent Degree in Electronics Engineering and Technology, and Computer Science with specialization in, Hardware Security, VLSI, &Microelectronics

The candidate desirably should have **B.Tech./B.E./M.Tech./ME/ Equivalent Degree in Electronics Engineering and Technology, and Computer Science with specialization in VLSI, Microelectronics and Semiconductor Devices or Allied area** with a minimum CPI of 6.5 or 60 % of marks. The upper age limit for applying for the JRF position shall be 32 years. Relaxations for SC/ST/OBC/women/PD will be given as per the GOI rules. Working/research experience in the field of **Device Simulations, TCAD tools and Proficiency in any Programming language** will be given preference. The selected candidates may get an **opportunity to enroll in PhD program** of the department after due process.

Interested and eligible candidates may appear for the **WALK-IN INTERVIEW (Online Mode)** on **25<sup>th</sup> August 2024, 11 am (Onwards)**. All candidates need to fill the Google form linked here <https://forms.gle/tpk8KEZsBamq54as7> and shortlisted will receive the separate Google meeting link before **24<sup>th</sup> August 2024**.

For any query contact Investigator(s): **Prof. Jawar Singh ([jawar@iitp.ac.in](mailto:jawar@iitp.ac.in))**, Dept. of Electrical Engineering, IIT Patna.

**Deputy Registrar**

Copy to:

1. Associate Dean, R&D, IIT Patna
2. Advertisement file
3. Project file



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**BIHTA PATNA-801106**  
**RESEARCH & DEVELOPMENT UNIT**

**PROJECT CODE: R&D/SP/EE/SERB/2021-22/624**

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**DATED:14.08.2024**

**FORMAT OF APPLICATION FOR JRF**

<b>Name &amp; Address</b> <b>Including email id and Phone no.</b> <b>(for Correspondence)</b>	<b>Category</b> (GEN/OBC/SC/S T/PD)	<b>DOB</b> dd/ mm/yy	<b>Professional Exam.</b> <b>(GATE/CSIR-NET, etc) &amp;</b> <b>Validity</b>
<b>NAME IN CAPITAL</b> Address:    Phone: Email:			
<b>Educational Qualification</b>			
<b>Institute/ Board</b>	<b>Exam Passed</b>	<b>Year of Passing</b>	<b>% of Marks/CPI</b>
	10 <sup>th</sup> Class		
	12 <sup>th</sup> Class		
	Bachelors (B.Sc/B.Tech/B.E./BCA) or equivalent		
	Masters (M.Sc/M.Tech/M.E/MCA/ MA) or equivalent		

<b>Qualifying degree</b>	<b>Degree/ major/Specialization</b>
(B.Sc/B.Tech/B.E./BCA)	
(M.Sc/M.Tech/M.E/MA/MCA)	
Others	

Signature of applicant

Date:

Place: