



INDIAN INSTITUTE OF TECHNOLOGY PATNA
BIHTA PATNA-801106
RESEARCH & DEVELOPMENT UNIT

ADVERTISEMENT NO: R&D/1008/DDN/438

DATED: 26.12.2024

Project No.: R&D/SP/EE/DDN/2023-24/1008

Applications are invited in the prescribed format only for the following assignment in a purely time bound research project undertaken in this institute.

1. (a) Name of the temporary assignment : **Junior Research Fellow (JRF)**
(b) Number of Post : **01 (one)**
(c) Duration of the Post : Initially for one year, extendable to another two years
Subject to satisfactory performance
2. Name of the temporary research project : **Design and Development of Neuromorphic Quantum Computing Architecture for High Throughput and Energy Efficient Applications**
3. Name of the sponsoring Agency : **DST, SERB**
4. Consolidated Fellowship/Salary: **37,000/- per month +HRA**
5. Qualifications & Experience : B.Tech./B.E./M.Tech./ME/ Equivalent Degree in Electronics Engineering and Technology, and Computer Science with specialization in, Hardware Security, VLSI, & Microelectronics

The candidate desirably should have **B.Tech./B.E./M.Tech./ME/ Equivalent Degree in Electronics Engineering and Technology, and Computer Science with specialization in VLSI, Microelectronics and Semiconductor Devices or Allied area** with a minimum CPI of 6.5 or 60 % of marks. The upper age limit for applying for the JRF position shall be 32 years. Relaxations for SC/ST/OBC/women/PD will be given as per the GOI rules. Working/research experience in the field of **Device Simulations, TCAD tools and Proficiency in any Programming language** will be given preference. The selected candidates may **get an opportunity to enroll in PhD program** of the department after due process.

Interested and eligible candidates are required to complete the Google Form available at <https://forms.gle/tpk8KEZsBamq54as7> by **13th January 2025**. Shortlisted candidates will receive the Google Meet link and detailed instructions for the online interview by **14th January 2025**

For any query contact Investigator(s): **Prof. Jawar Singh (jawar@iitp.ac.in)**, Dept. of Electrical Engineering, IIT Patna.

Deputy Registrar

Copy to:

1. Associate Dean, R&D, IIT Patna
2. Advertisement file
3. Project file



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PROJECT CODE: R&D/SP/EE/SERB/2021-22/624

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FORMAT OF APPLICATION FOR JRF

Name & Address Including email id and Phone no. (for Correspondence)	Category (GEN/OBC/SC/S T/PD)	DOB dd/ mm/yy	Professional Exam. (GATE/CSIR-NET, etc) & Validity
NAME IN CAPITAL Address: Phone: Email:			
Educational Qualification			
Institute/ Board	Exam Passed	Year of Passing	% of Marks/CPI
	10 th Class		
	12 th Class		
	Bachelors (B.Sc/B.Tech/B.E./BCA) or equivalent		
	Masters (M.Sc/M.Tech/M.E/MCA/ MA) or equivalent		

Qualifying degree	Degree/ major/Specialization
(B.Sc/B.Tech/B.E./BCA)	
(M.Sc/M.Tech/M.E/MA/MCA)	
Others	

Date:

Place:

Signature of applicant